

FIGURE 1A

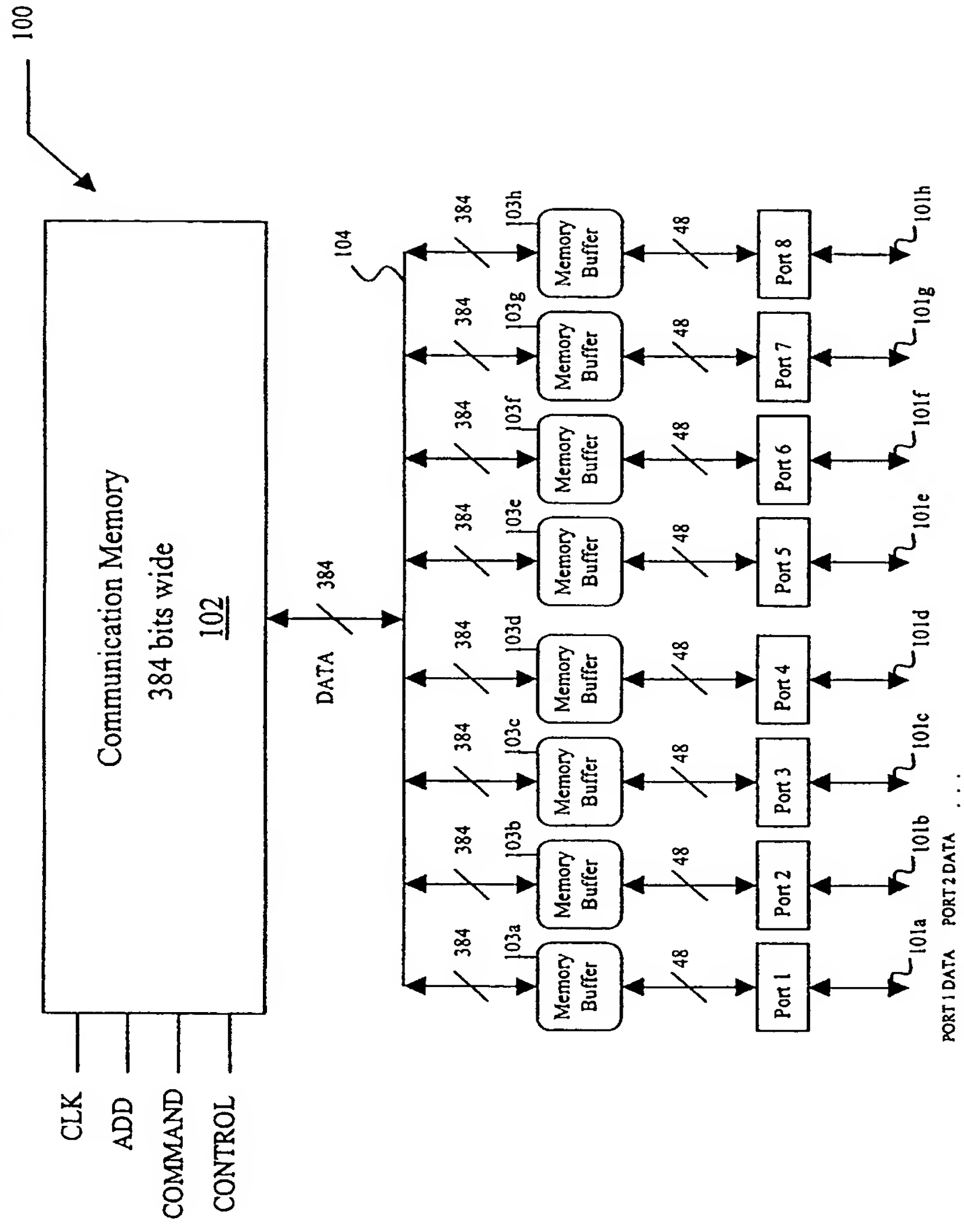
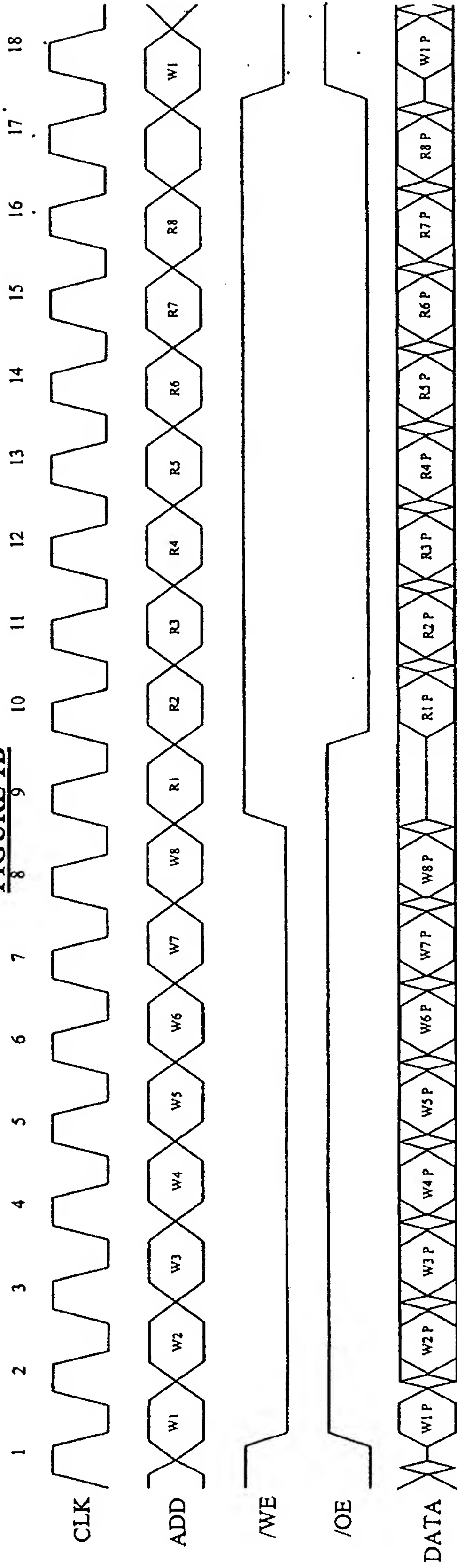
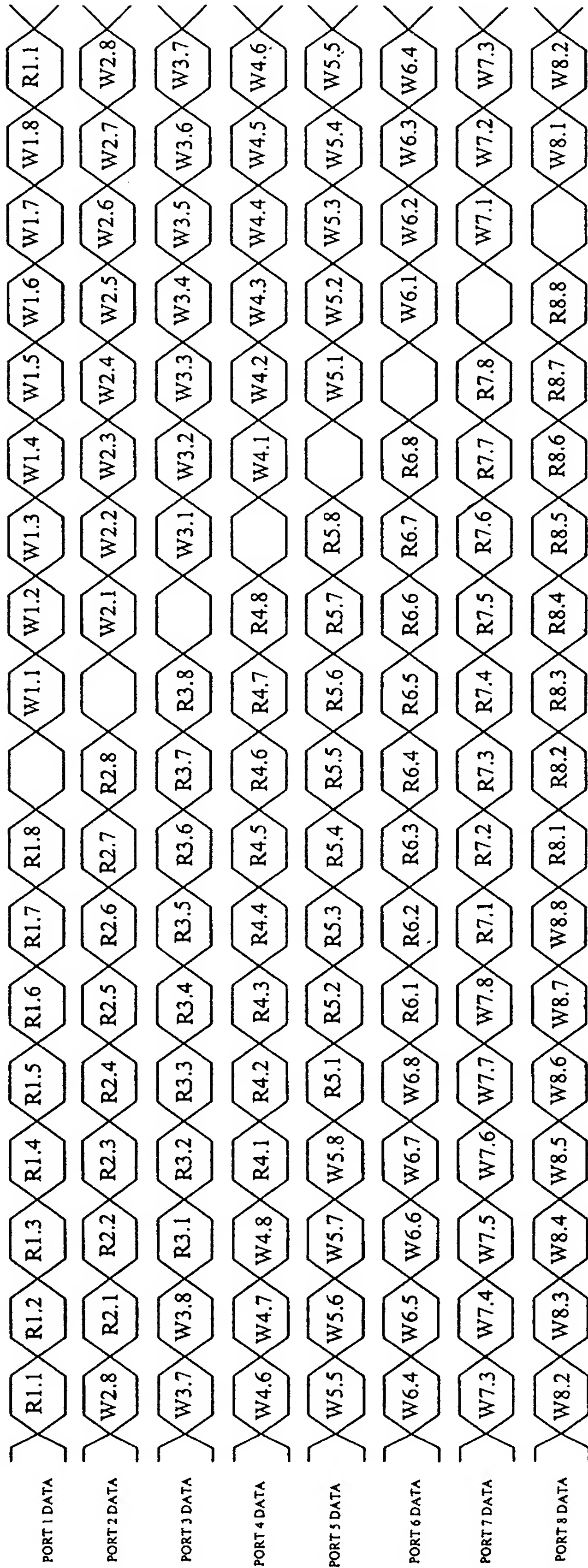


FIGURE 1B



SHARED MEMORY



MEMORY BUFFER

Nomenclature:

3859821

17200-P043US

R- Data moving out of the switch
W- Data moving into the switch

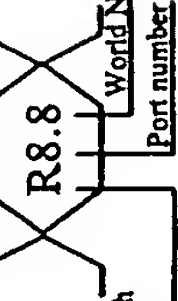


FIGURE 2A

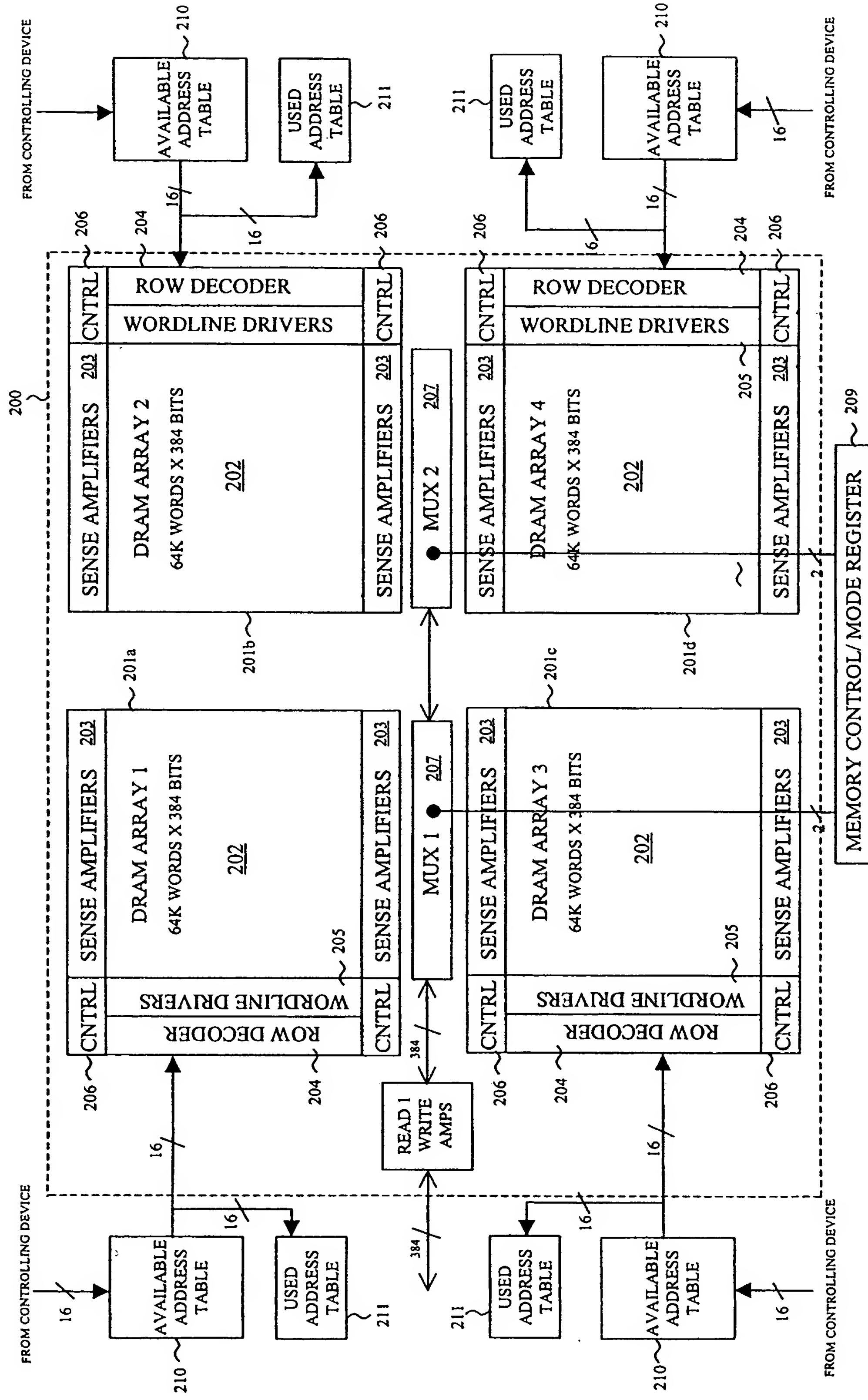
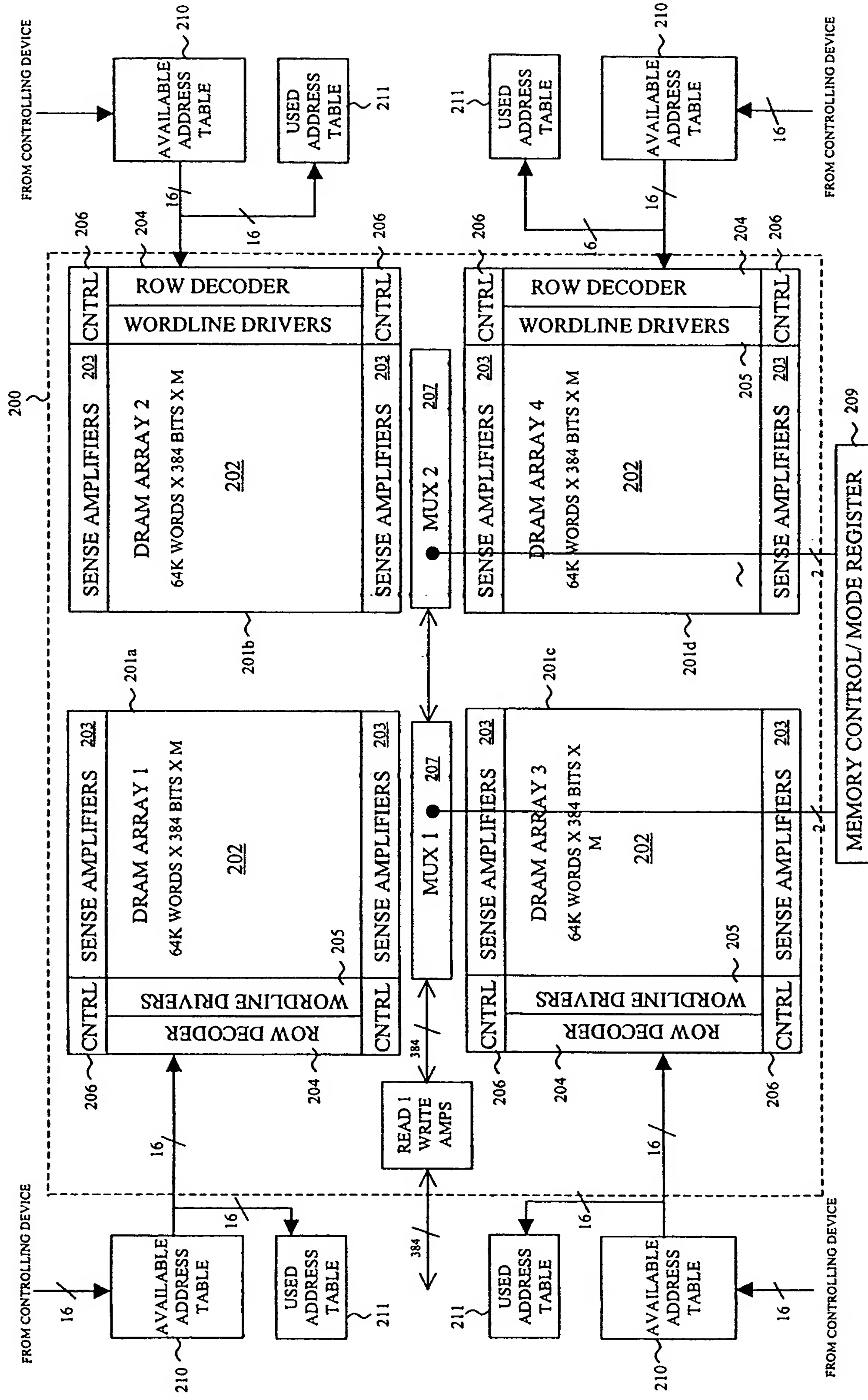
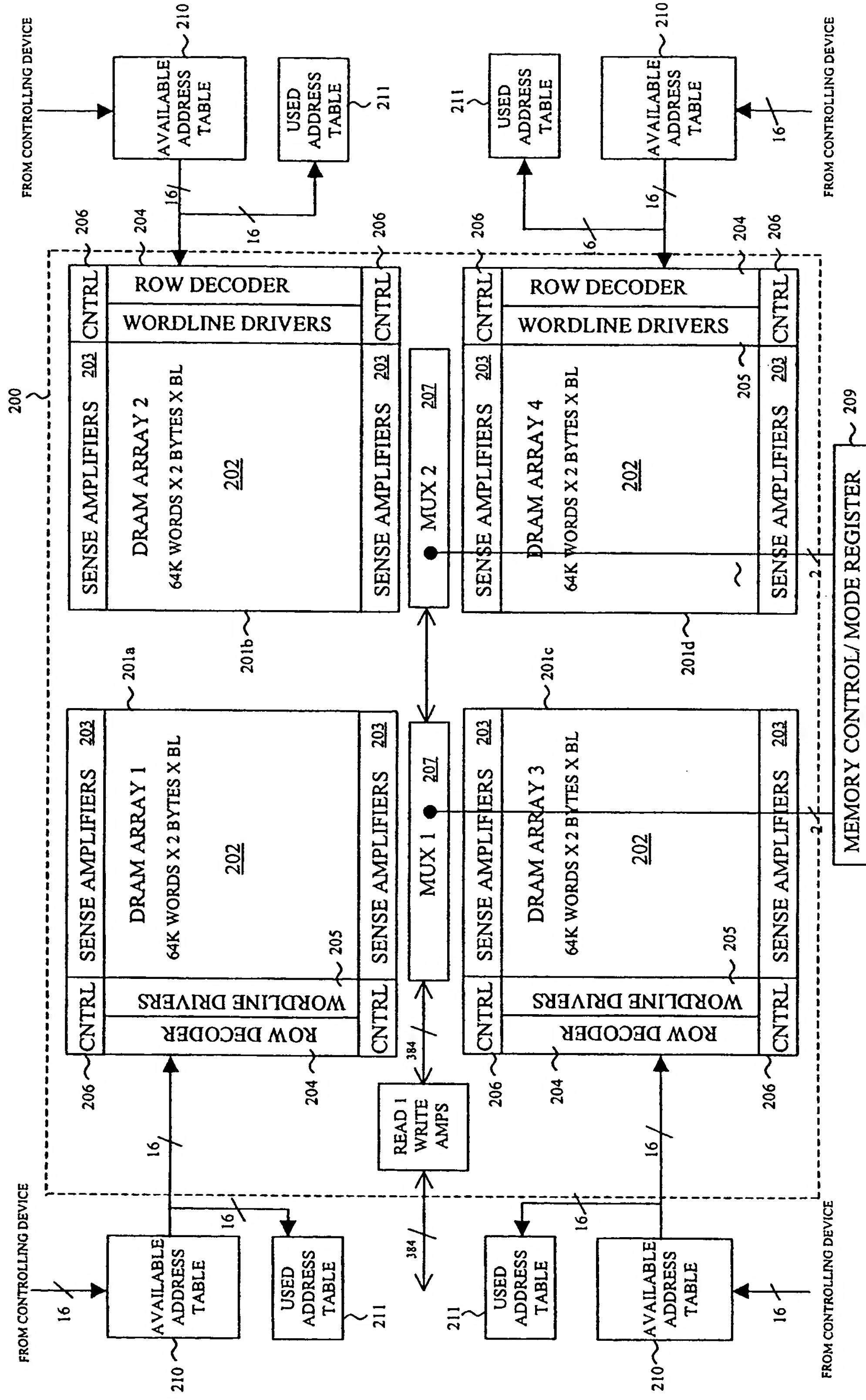


FIGURE 2B



'M' signifies the number of "column decoder groups".

FIGURE 2C



BL = 2, 4, 6, 8, 16 where BL represents burst length.

FIGURE 3A

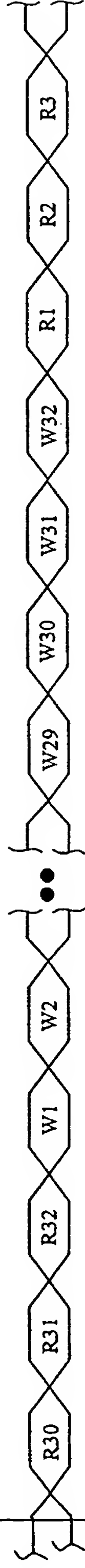


FIGURE 3B

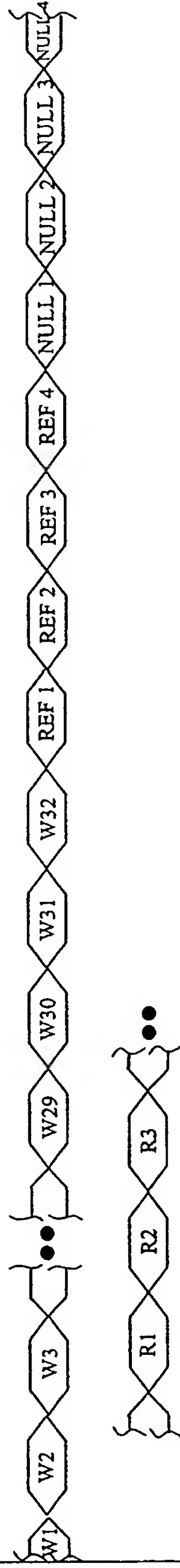


FIGURE 3C

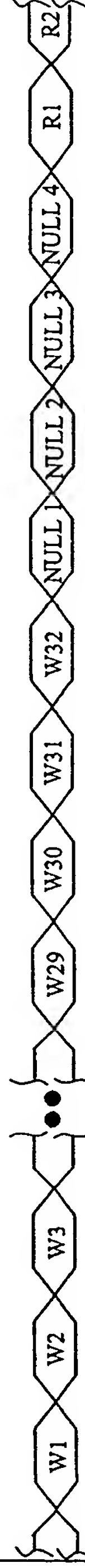


FIGURE 4

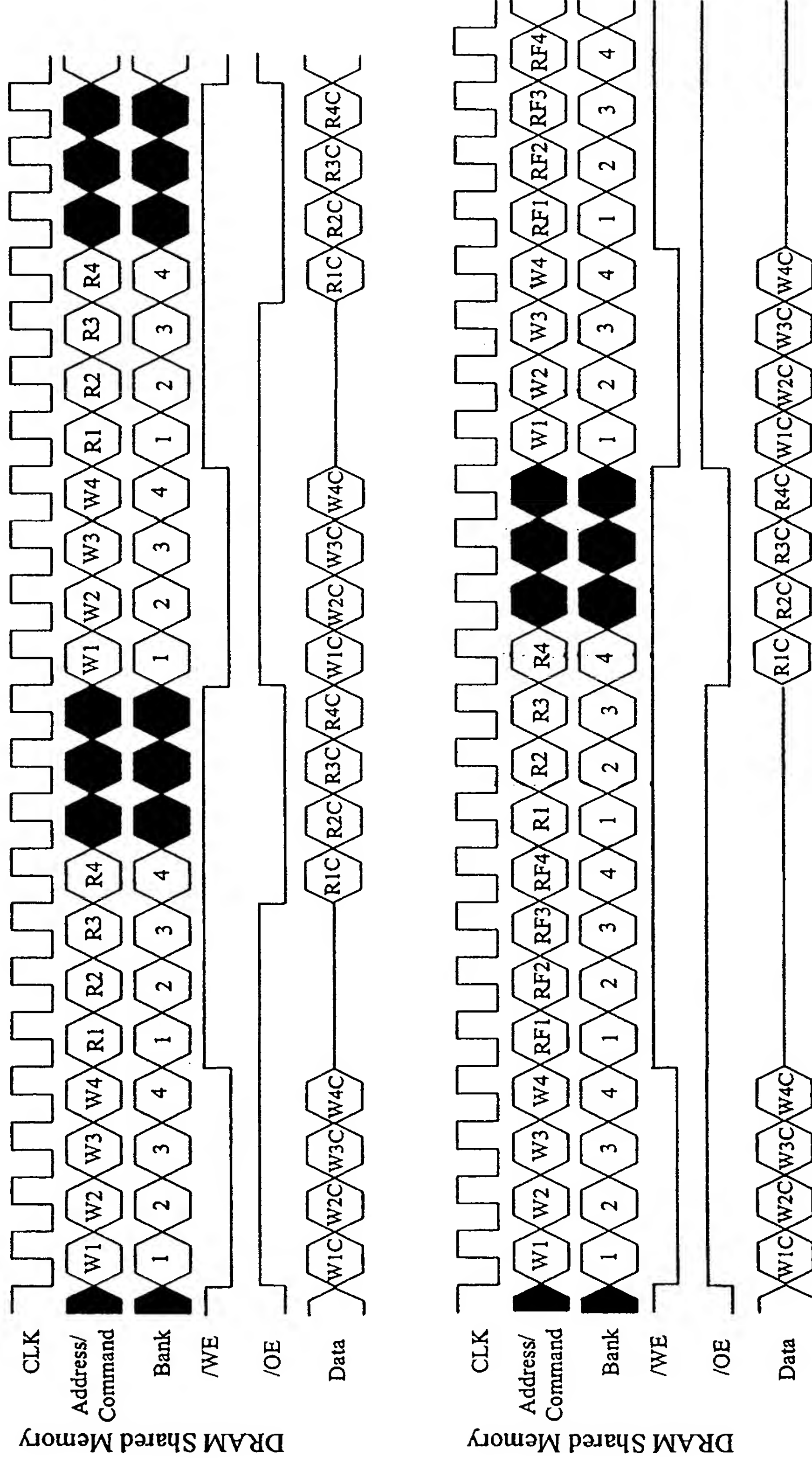


FIGURE 5

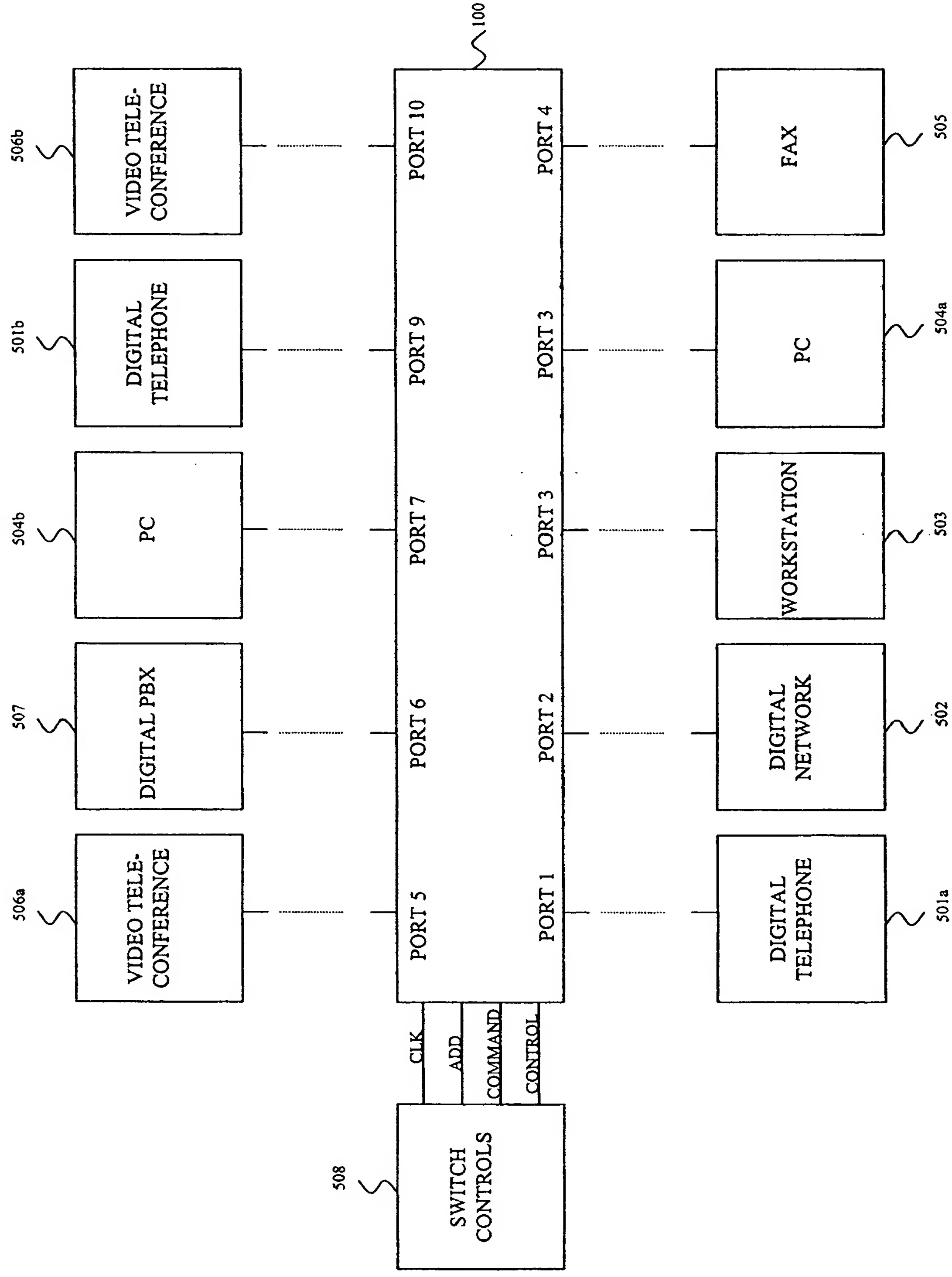


FIGURE 6

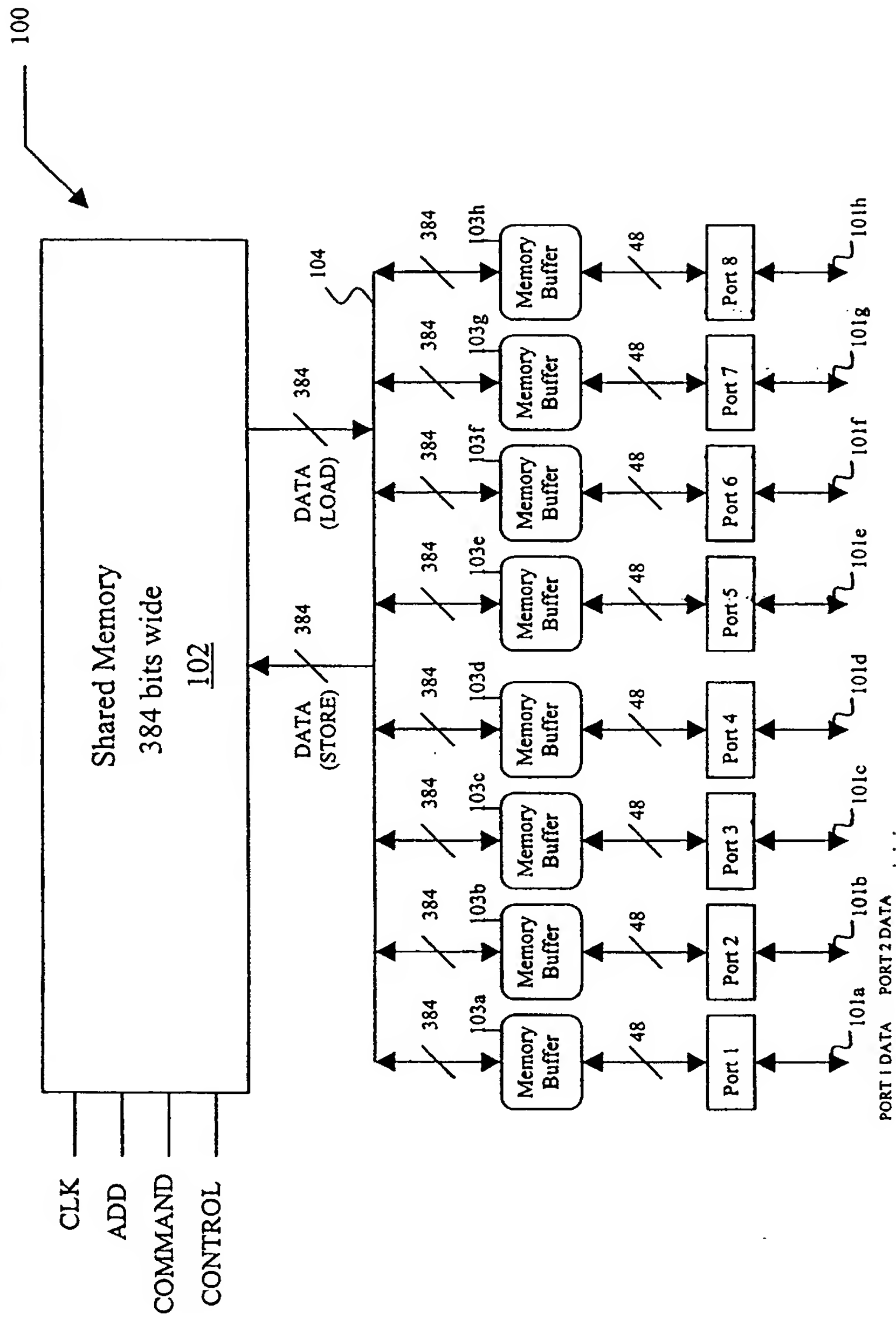


FIGURE 7A

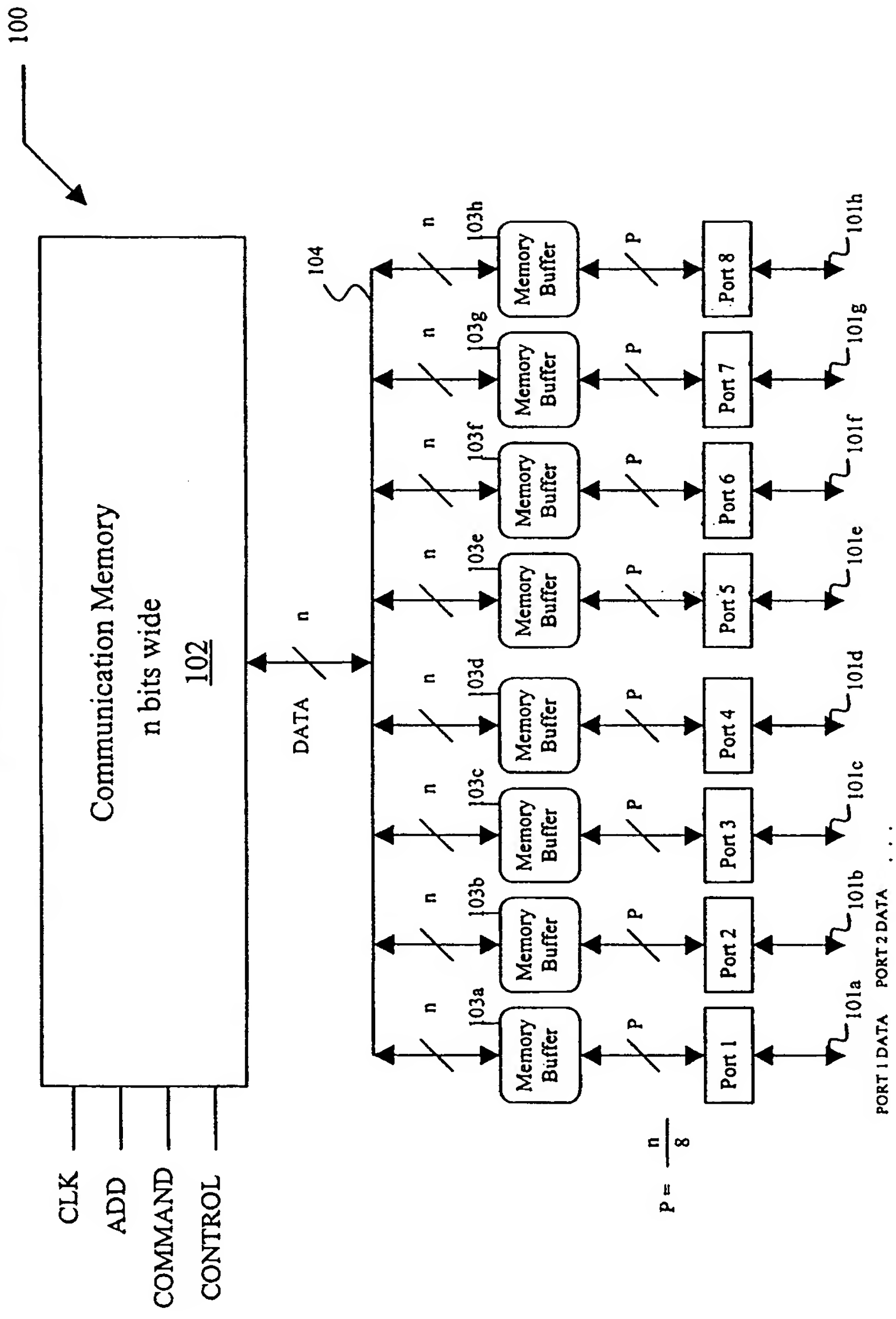
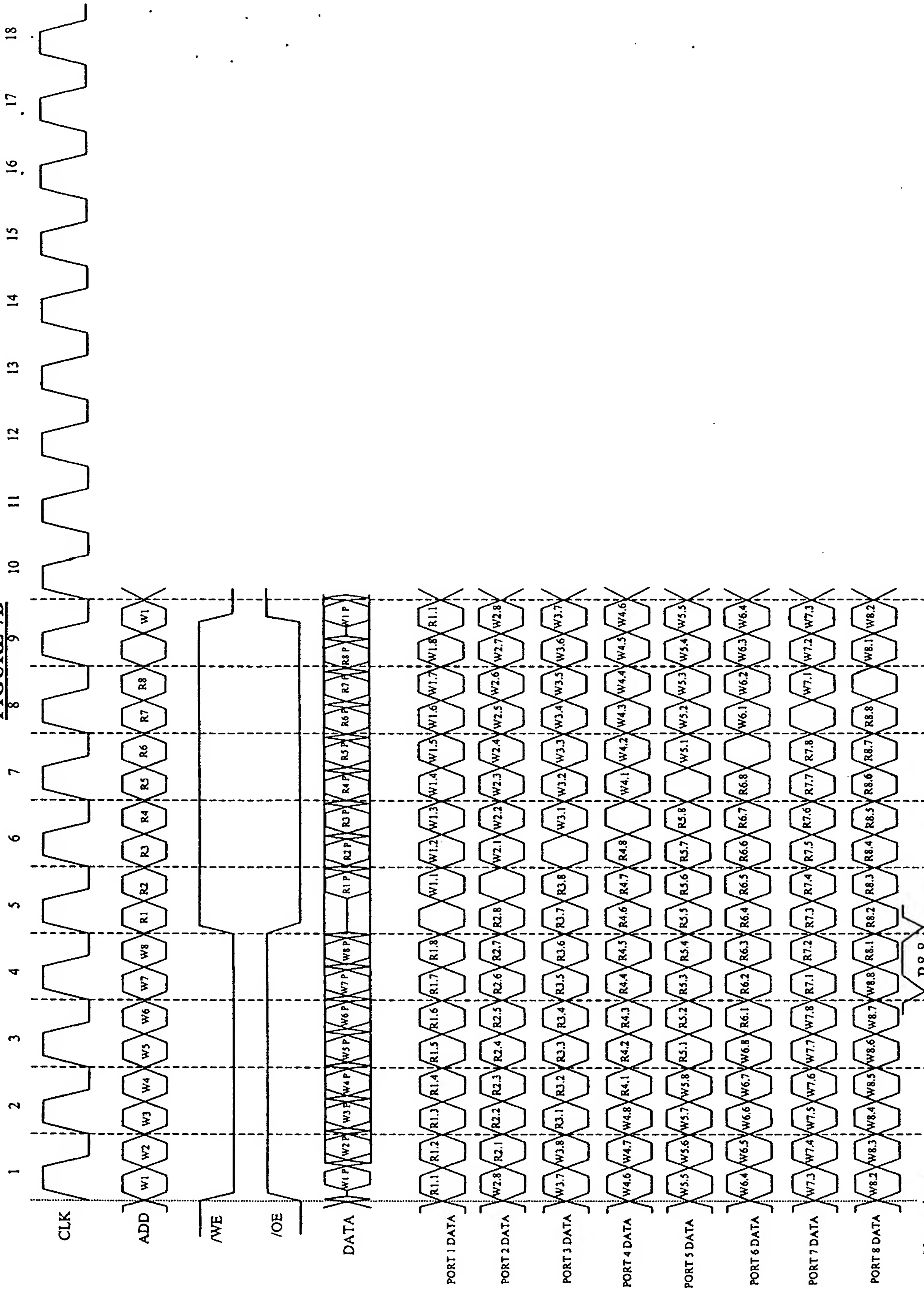


FIGURE 7B



Nomenclature:

3859821

17200-P043US

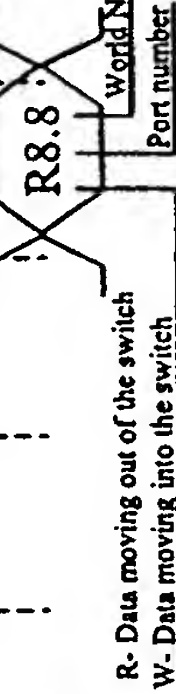


FIGURE 8A

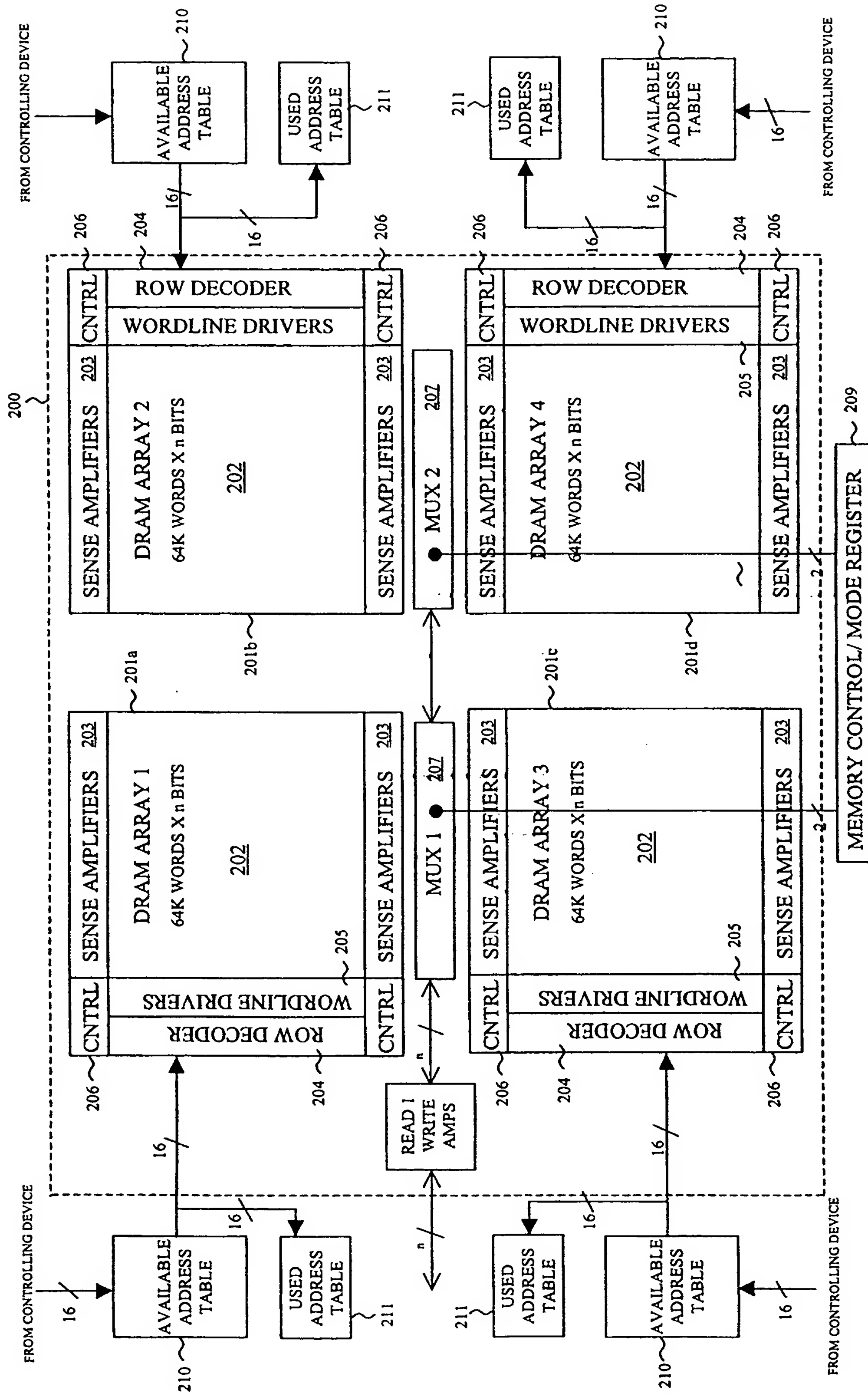


FIGURE 8B

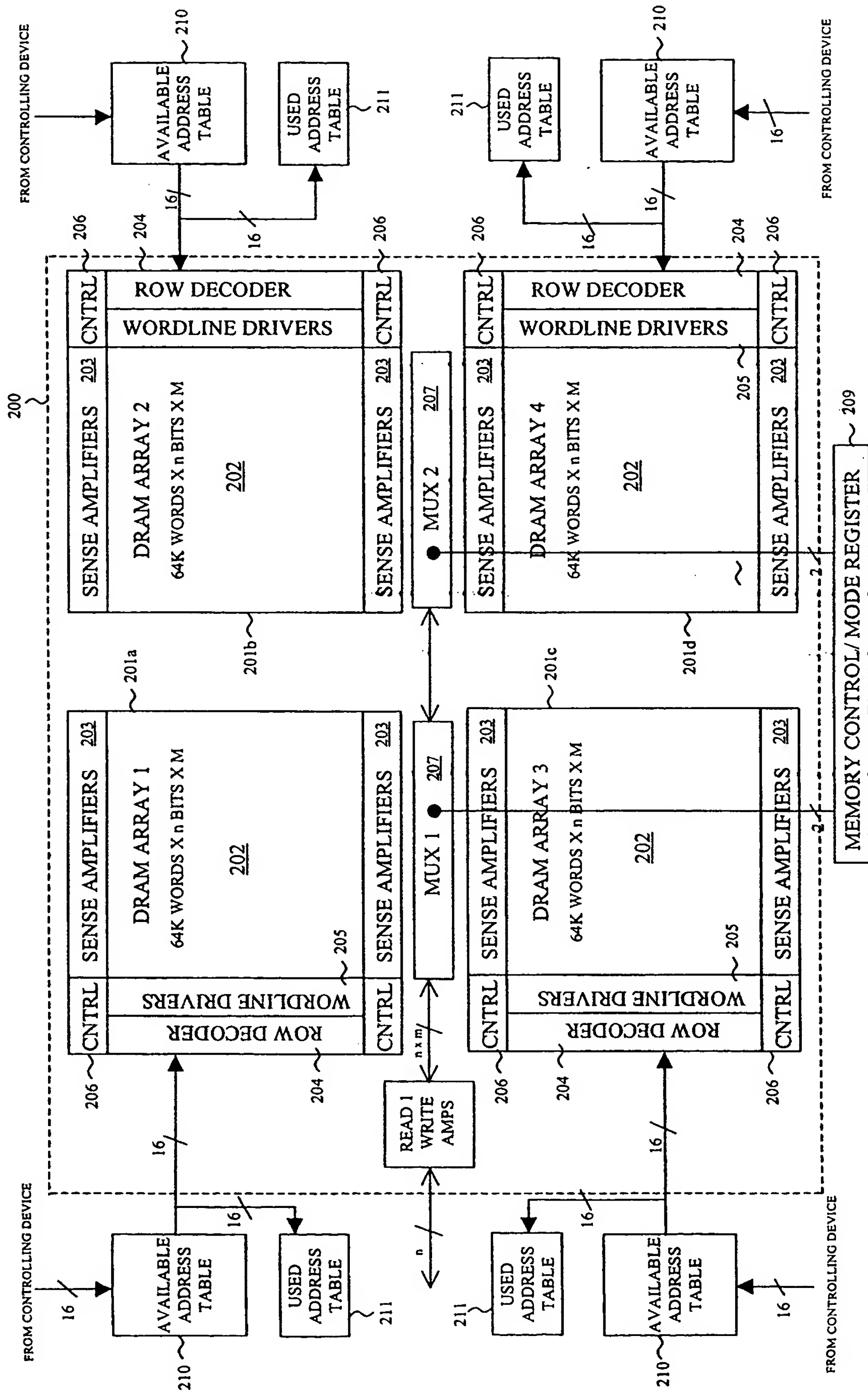


FIGURE 8C

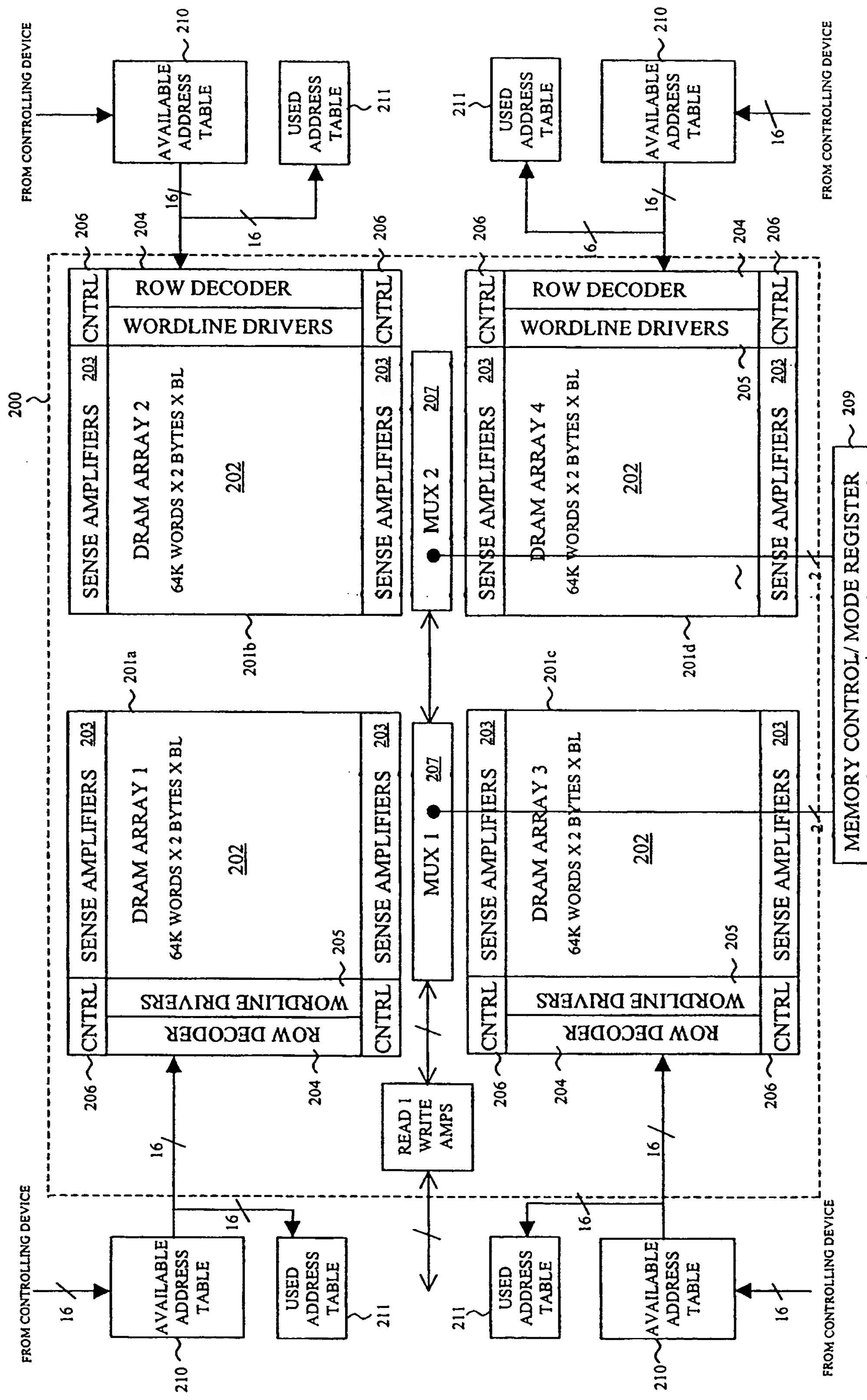


FIGURE 9A

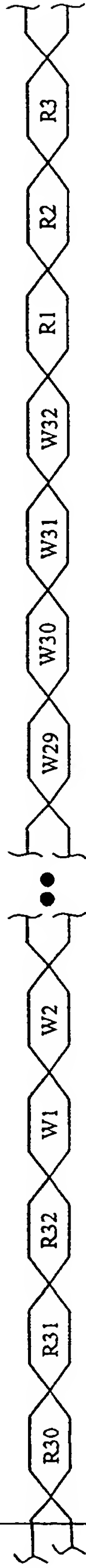


FIGURE 9B

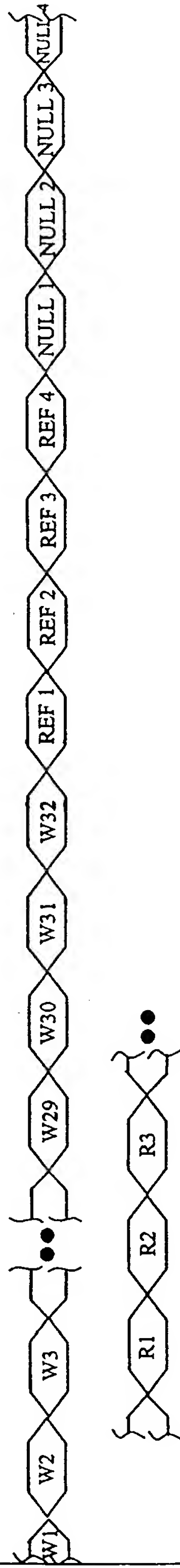


FIGURE 9C

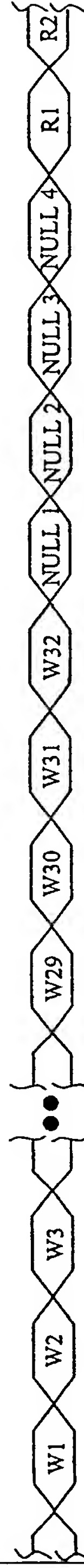


FIGURE 10

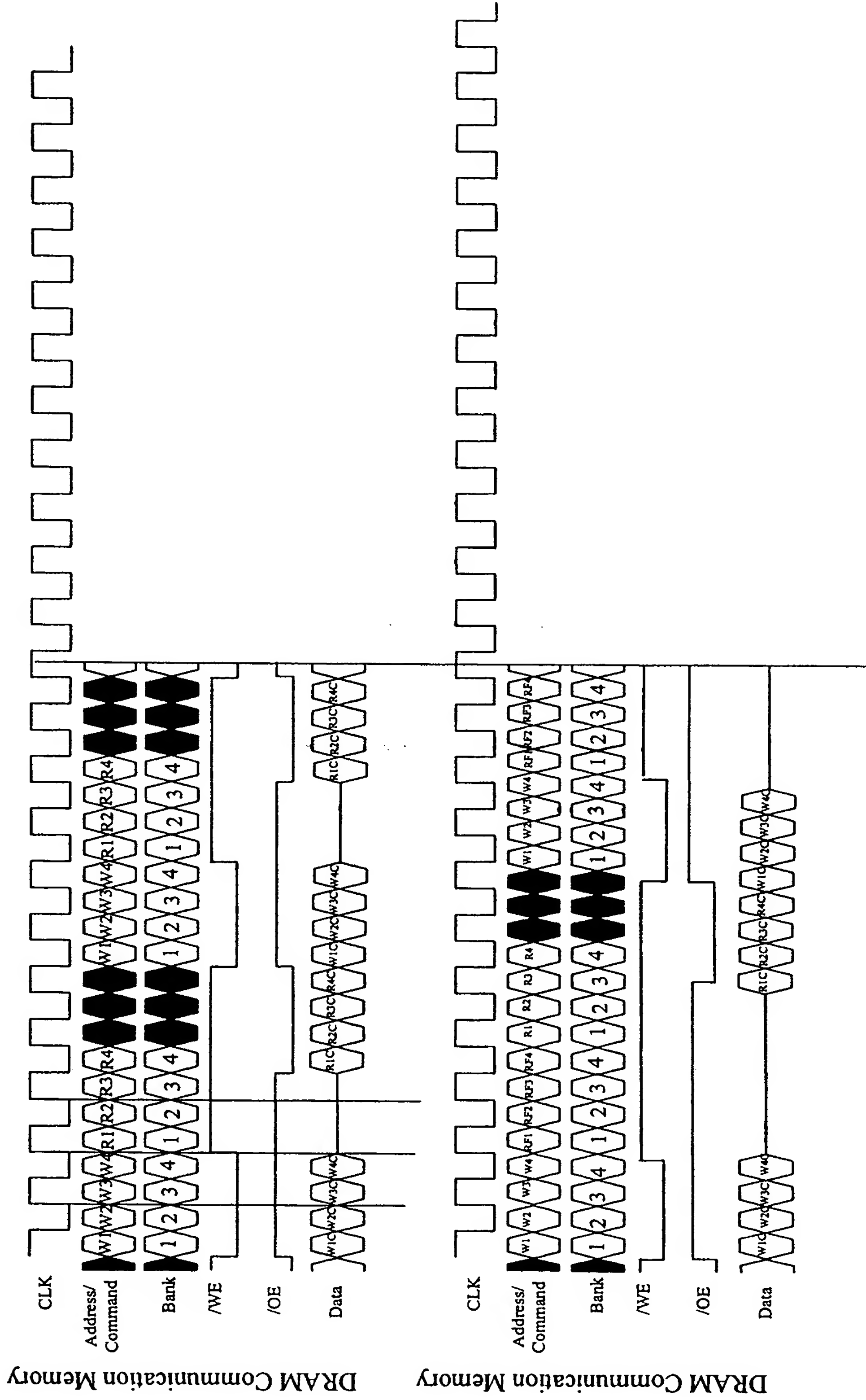


FIGURE 11

